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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/590,405	10/30/2007	Franciscus J. Klosters	NL04 0241 US1	8682
65913 NXP , B.V.	7590 05/07/201	EXAMINER		
NXP INTELLECTUAL PROPERTY & LICENSING M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131			GUYTON, PHILIP A	
			ART UNIT	PAPER NUMBER
			2113	
			NOTIFICATION DATE	DELIVERY MODE
			05/07/2010	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

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		Application No.	Applicant(s)	
Office Action Comments		10/590,405	KLOSTERS, FRANCISCUS J.	
	Office Action Summary	Examiner	Art Unit	
		PHILIP GUYTON	2113	
Period fo	The MAILING DATE of this communication ap r Reply	opears on the cover sheet with the c	orrespondence address	
A SHO WHIC - Exten after: - If NO - Failur Any n	DRTENED STATUTORY PERIOD FOR REPI HEVER IS LONGER, FROM THE MAILING I sions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. period for reply is specified above, the maximum statutory period e to reply within the set or extended period for reply will, by statu- aply received by the Office later than three months after the mailind d patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION .136(a). In no event, however, may a reply be timed will apply and will expire SIX (6) MONTHS from the, cause the application to become ABANDONEI	lely filed the mailing date of this communication. (35 U.S.C. § 133).	
Status				
 Responsive to communication(s) filed on <u>15 April 2010</u>. This action is FINAL. 2b) This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i>, 1935 C.D. 11, 453 O.G. 213. 				
Dispositi	on of Claims			
5)□ 6)⊠ 7)□ 8)□	Claim(s) <u>1-6</u> is/are pending in the application 4a) Of the above claim(s) is/are withdraydlaim(s) is/are allowed. Claim(s) <u>1-6</u> is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/	awn from consideration.		
	on Papers			
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 				
Priority u	nder 35 U.S.C. § 119			
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 				
2) Notice 3) Inform	e of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO/SB/08) No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ite	

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DETAILED ACTION

Response to Amendment

1. In view of the amendments to the claims, the declaration filed on 15 April 2010 under 37 CFR 1.131 is sufficient to overcome the Richmond reference.

However, upon further consideration, a new ground of rejection is made in view of Starr.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1-4 and 6 are rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 6,891,401 to Starr et al. (hereinafter Starr).

With respect to claim 1, Starr discloses an electronic circuit arrangement comprising:

a clock fail circuit (figure 1, item 30 – clock sense circuit) arranged to receive a clock signal and to generate an error signal upon an absence of the clock signal (column 2, lines 62-64); and

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an asynchronous processor (figure 1, item 14 – switchover circuit) arranged to receive said error signal and to bring the electronic circuit arrangement into a predefined state upon detection of the error signal (column 3, lines 18-22), wherein the asynchronous processor remains dormant in the absence of a clock failure event (column 3, lines 32-37 – toggles switches when clock failure signal is received).

With respect to claim 2, Starr discloses in that the asynchronous processor comprises an interrupt input for receiving the error signal and is further arranged to execute software instructions upon reception of the signal (column 3, lines 32-37).

With respect to claim 3, Starr discloses an integrated circuit comprising an electronic circuit arrangement as claimed in claim 1 (figure 1).

With respect to claim 4, Starr discloses a bus station for use in a bus system comprising an electronic circuit arrangement as claimed in claim 1 (figure 1 and column 2, lines 48-51 and column 3, lines 32-37).

With respect to claim 6, Starr discloses a method for bringing an electronic circuit arrangement into a predetermined state, the method comprising:

detecting an absence of a clock signal using a clock fail circuit (column 1, lines 53-56 and figure 1, item 30);

generating an error signal in response to the absence of the clock signal (column 2, lines 62-64); and

bringing the electronic circuit arrangement into the pre-defined state (column 3, lines 18-22) using an asynchronous processor within the electronic circuit arrangement

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(figure 1, item 14), wherein the asynchronous processor remains dormant in the absence of a clock failure event (column 3, lines 32-37).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Starr in view of U.S. Patent No. 6,959,014 to Pohlmeyer et al. (hereinafter Pohlmeyer).

Starr does not disclose expressly wherein the bus station is a bus station for use in a LIN bus system.

However, Pohlmeyer teaches determination of synchronization between transmitters and receivers in a LIN bus system (abstract and column 1, lines 12-27).

At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to modify Starr for use on a LIN bus system, as taught by Pohlmeyer. A person of ordinary skill in the art would have been motivated to do so because it is necessary to retain synchronization between nodes in a LIN bus system, as disclosed by Pohlmeyer (column 1, lines 22-27). Thus, loss of clock, or clock error would be highly detrimental in a LIN bus system (Pohlmeyer – column 2, lines 41-49 and column 4, lines 1-15). Starr teaches a custom integrated circuit with clock fault determination (column 1, lines 19-34 and lines 53-57), which would have been highly

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integratable with the LIN bus system of Pohlmeyer, which teaches multiple microcontrollers and a bus system (column 2, lines 62-64).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to PHILIP GUYTON whose telephone number is (571)272-3807. The examiner can normally be reached on M-F 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/Philip Guyton/ Primary Examiner, Art Unit 2113